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Approved for use through 10/31/2002. OMB 0651-0031

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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/759,865
	Filing Date	01/15/2004
	First Named Inventor	Minoru Fujishima
	Group Art Unit	Unknown
	Examiner Name	Unknown
Total Number of Pages in This Submission	Attorney Docket Number	44471/296251

ENCLOSURES (check all that apply)		
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Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Robert E. Richards, Reg. No. 29,105 KILPATRICK STOCKTON LLP
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PATENTS

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Minoru Fujishima

Serial No. 10/759,865

Filed: January 15, 2004

For: Frequency Divider, PLL Circuit And
Semiconductor Integrated Circuit

Art Unit: Not Yet Assigned

Examiner: Not Yet Assigned

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Robert E. Richards Reg. No. 29,105

INFORMATION DISCLOSURE STATEMENT

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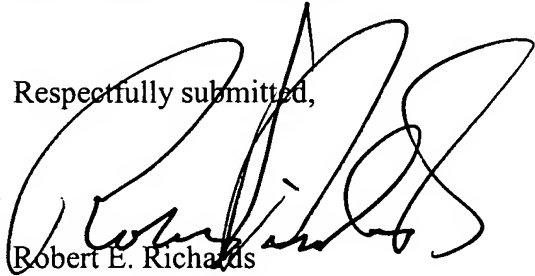
Sir:

The citation of information on the attached Form PTO/SB/08A, "List of Art Cited by Applicant" is made pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98. A copy of each non-U.S. patent document listed is enclosed.

Information Disclosure Statement
Serial No. 10/759,865

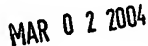
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Respectfully submitted,



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First Named Inventor	Minoru Fujishima
Group Art Unit	Not yet assigned
Examiner Name	Not yet assigned
Attorney Docket Number	44471/296251

Sheet	1	of	2
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U.S. PATENT DOCUMENTS

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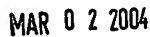
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OTHER INFORMATION - NON PATENT LITERATURE DOCUMENTS

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